

Claim 1 recites a first control portion for outputting a signal for interrupting a high-voltage power supply and a safing signal for interrupting a high-voltage power supply in response to an output of said semiconductor impact sensor.

As discussed during the personal interview, and agreed to by the Examiners as indicated in the Examiner's Interview Summary, the combination of Hayakawa and Nakashima fail to teach the above-mentioned features.

Therefore, it is respectfully submitted that independent claim 1 is allowable. It is also submitted that dependent claims 3 and 4 are allowable for at least their dependence on allowable claim 1, and their distinct patentable features recited therein.

It is respectfully requested that the rejection be withdrawn.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of the claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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